Dr. Wei-Tsu Tseng

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Wei- Tseng is a senior research staff member in IBM's semiconductor research center in Albany, NY, USA. He has accrued 18 years of experience in IBM and has been leading the CMP R&D activities including process integration, consumable evaluation, post CMP cleaning, and JDP projects with equipment and chemical suppliers. Prior to his current position at IBM, he was a senior technical staff at GlobalFoundries as a CMP team lead in Advanced Technology Development (ATD) department from 2015 to 2018. Before joining IBM's semiconductor research & development center in East Fishkill, NY in 2001, he was an assistant professor in National Chiao Tung University and then National Cheng Kung University in Taiwan.

Wei-Tsu has to his credits more than 50 patents and has published 100+ technical papers covering all different technical aspects of CMP, post-CMP cleaning, low-k dielectrics, Cu plating and interconnects, and am often invited to give technical presentations worldwide at major integrated circuit (IC) device processing related international conferences.

Wei-Tsu obtained my Ph.D. from the University of Texas at Austin in Materials Science and Engineering. B.S. degree in Metallurgy and Material Science was from in the National Cheng Kung University in Taiwan.