

Korean International Semiconductor Conference on Manufacturing Technology 2022 (KISM 2022) November 13-16, 2022 Paradise Hotel Busan (Haeundae Beach), Busan, Korea



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Dr. Sohn is the VP of Technology in Samsung Electronics Semiconductor Memory Division. He is working at Memory Manufacturing Center leading Advanced Metrology & Inspection (MI) team. Since joining Samsung in 2006, he has been involved in the development of advanced MI technology projects from discovering solution to implementing into massive production system. He holds more than 30 patents in the field of semiconductor MI.

Dr. Sohn received his Ph.D. from Northwestern University (mechanical engineering) in 2005. The thesis title was "Near-field photo-acoustic materials characterization using scanning laser source and micro-fabricated ultrasound receiver."