

Korean International Semiconductor Conference on Manufacturing Technology 2022 (KISM 2022) November 13-16, 2022/Paradise Hotel Busan, Busan, Korea



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Since 2019, he has been with R&D Process at SK Hynix, where he is currently leading the development of dielectric thin film processes for DRAM. Previously he worked on the development of a wide range of metal and dielectric thin film deposition and assembly processes at Intel, Applied Materials, and Lam Research. He holds a Ph.D. in Materials Science and Engineering from Massachusetts Institute of Technology (MIT).